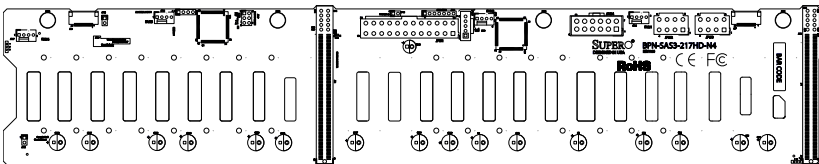




BPN-SAS3-217HD-N4 BACKPLANE



USER'S GUIDE

1.0

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WARNING: Handling of lead solder materials used in this product may expose you to lead, a chemical known to the State of California to cause birth defects and other reproductive harm.

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Contents

Contacting Supermicro.....	iv
Returning Merchandise for Service.....	v
Chapter 1 Guidelines	
1-1 ESD Safety Guidelines	1-1
1-2 General Safety Guidelines	1-1
1-3 Version Information	1-2
Chapter 2 Connectors, Jumpers, and LEDs	
2-1 Rear Connectors	2-1
2-2 Rear Connector Definitions	2-2
2-3 Rear Jumper	2-3
Explanation of Jumpers	2-3
2-4 Front Connectors and LED Indicators	2-4

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Returning Merchandise for Service

A receipt or copy of your invoice marked with the date of purchase is required before any warranty service will be rendered. You can obtain service by calling your vendor for a Returned Merchandise Authorization (RMA) number. When returning to the manufacturer, the RMA number should be prominently displayed on the outside of the shipping carton, and mailed prepaid or hand-carried. Shipping and handling charges will be applied for all orders that must be mailed when service is complete.

For faster service, RMA authorizations may be requested online (<http://www.supermicro.com/support/rma/>).

Whenever possible, repack the backplane in the original Supermicro box, using the original packaging materials. If these are no longer available, be sure to pack the backplane in an anti-static bag and inside the box. Make sure that there is enough packaging material surrounding the backplane so that it does not become damaged during shipping.

This warranty only covers normal consumer use and does not cover damages incurred in shipping or from failure due to the alteration, misuse, abuse or improper maintenance of products.

During the warranty period, contact your distributor first for any product problems.

Notes

Chapter 1

Guidelines

This chapter offers guidelines for personal and equipment safety, and notes about the BPN-SAS3-217HD-N4 version documented in this manual.

1-1 ESD Safety Guidelines

Electrostatic Discharge (ESD) can damage electronic components. To prevent damage to your system, it is important to handle it very carefully. The following measures are generally sufficient to protect your equipment from ESD.

- Use a grounded wrist strap designed to prevent static discharge.
- Touch a grounded metal object before removing a component from the antistatic bag.
- Handle the backplane by its edges only; do not touch its components, peripheral chips, memory modules or gold contacts.
- When handling chips or modules, avoid touching their pins.
- Put the card and peripherals back into their antistatic bags when not in use.

1-2 General Safety Guidelines

- Always disconnect power cables before installing or removing any components from the computer, including the backplane.
- Disconnect the power cable before installing or removing any cables from the backplane.
- Make sure that the backplane is securely and properly installed on the motherboard to prevent damage to the system due to power shortage.

1-3 Version Information

The BPN-SAS3-217HD-N4 backplane has been designed to utilize the most up-to-date technology available, providing your system with reliable, high-quality performance.

This manual reflects BPN-SAS3-217HD-N4, Revision 1.00, the most current release available at the time of publication. Refer to the Supermicro Web site at www.supermicro.com for the latest updates, compatible parts and supported configurations.

Chapter 2

Connectors, Jumpers and LEDs

This manual covers BPN-SAS3-217HD-N4 with NVMe capabilities.

2-1 Rear Connector Locations

The following connectors are on the side of the backplane that faces the rear of the chassis. They are marked by silkscreen labels.

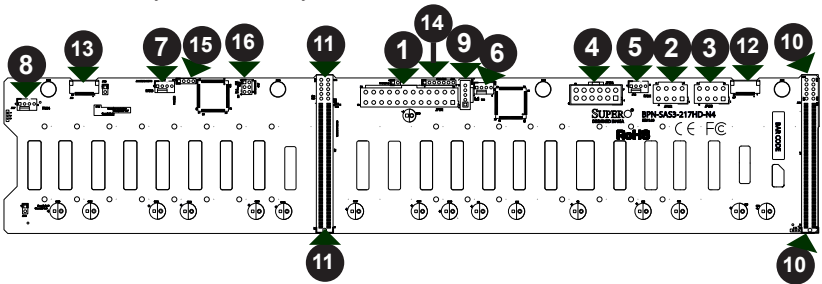


Figure 2-1. Rear Connectors

- | | |
|--|---|
| 1. Main Power Connector: JPW1 | 9. Power Supply SM Bus Connector: JPI2C1 (5-pin) |
| 2. Secondary Power Connector: JPW2 (2x4 pin) | 10. Node A connector: JF1 |
| 3. Secondary Power Connector: JPW3 (2x4 pin) | 11. Node B connector: JF2 |
| 4. Secondary Power Connector: JPW4 (2x6 pin) | 12. Backplane to front panel connector for node A: J27 (20-pin) |
| 5. Chassis Fan Connector: Fan1 JP54 (4-pin) | 13. Backplane to front panel connector for node B: J28 (20-pin) |
| 6. Chassis Fan Connector: Fan2 JP55 (4-pin) | 14. CPLD Program Port: JP70 (7-pin) |
| 7. Chassis Fan Connector: Fan3 JP56 (4-pin) | 15. MCU Serial port: J25 (4-pin) |
| 8. Chassis Fan Connector Fan4 JP57 (4-pin) | 16. MCU firmware upgrade port: JP69 (6-pin) |

2-2 Rear Connector Definitions

1-4. Power Supply Connectors

These connectors, designated JPW1, JPW2, JPW3, and JPW4, supply power to the two motherboard nodes and to the 24 hard drives.

5-8. Chassis Fan Connectors

These connectors, designated JP54, JP55, JP56 and JP57 supply power to the cooling fans.

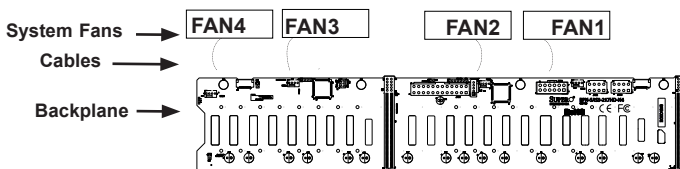


Figure 2-2. Default Configuration--Fans Connected Directly to the Backplane

9. Power Supply SM Bus Connector

The 5-pin connector, designated JPI2C1, connects the power supply SMBus to the MCU.

10-11. Motherboard to Backplane Connectors

JF1 connects the Node 1 motherboard to the backplane; JF2 connects the Node 2 motherboard.

12-13. Backplane to Front Panel Headers

J27 and J28 connect cables to the chassis front control panels--J27 connects to the panel on the left side of the chassis, J28 connects to the right side.

14. CPLD Program Port

JP70 is used to update the CPLD firmware.

15. MCU Serial Port

J25 is a port to debug the MCU firmware.

16. MCU Upgrade Port

JP69 is a port to upgrade the MCU firmware.

2-3 Rear Jumpers and Pin Definitions

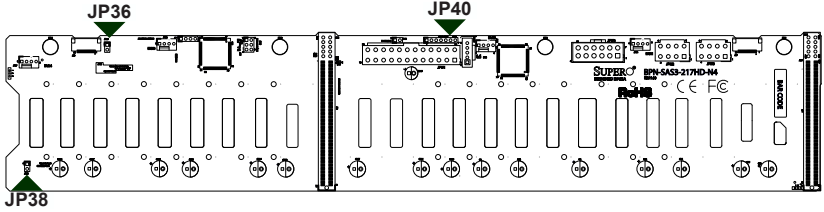
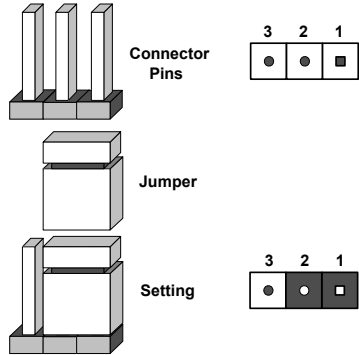


Figure 2-3. Rear Jumpers

Explanation of Jumpers

To modify the operation of the backplane, jumpers can be used to choose between optional settings. Jumpers create shorts between two pins to change the function of the connector. Pin 1 is identified with a square solder pad on the printed circuit board. Note: On two pin jumpers, "Closed" means the jumper is on and "Open" means the jumper is off the pins.



Jumper Settings	
Jumper	Jumper Settings
JP36	Open: Default Closed: Any one button for power on
JP38	Manufacturer testing only
JP40	Open: Default Closed: Power on

2-3 Rear LED Indicators

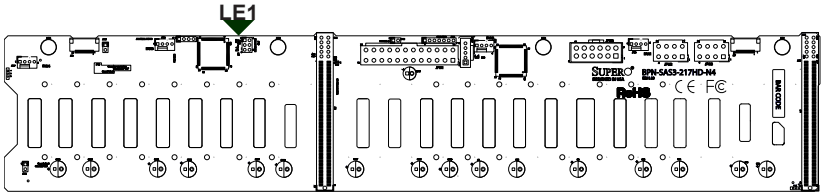


Figure 2-4. Rear LEDs

Rear LEDs		
LED	State	Specification
LE1	Blinking	MCU Heartbeat

2-4 Front Connectors and LED Indicators

SAS/SATA/NVMe

The front of the backplane has 24 sockets to connect hard disk drives, along with LEDs indicators. The BPN-SAS3-217HD-N4 is designed to work with two separate computing nodes, each with an independent motherboard. The backplane supports twelve drives for each node. The drive ports are designated A0-A11 and B0-B11, where A represents one node and B the other.

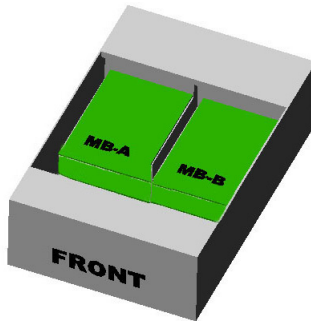


Figure 2-5. Motherboard Locations In the Chassis

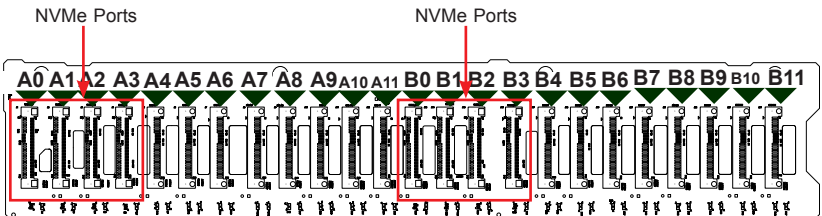


Figure 2-6. Front Connectors

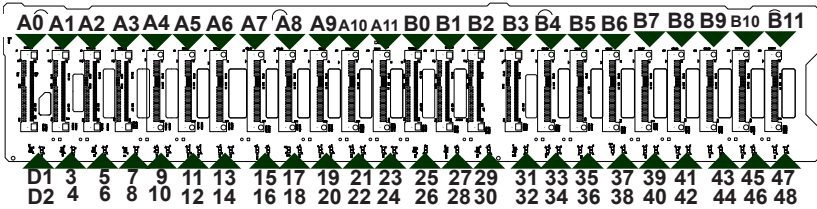


Figure 2-7. Front Connectors and LEDs

Drive Connectors and LED Indicators			
Drive Number	Label	HDD Activity LED (blue)	Failure LED (red/amber/green**)
NVMe #A0*	J1	ACT#A0	FAIL#A0**
NVMe #A1*	J2	ACT#A1	FAIL#A1**
NVMe #A2*	J3	ACT#A2	FAIL#A2**
NVMe #A3*	J4	ACT#A3	FAIL#A3**
SAS #A4	J5	ACT#A4	FAIL#A4
SAS #A5	J6	ACT#A5	FAIL#A5
SAS #A6	J7	ACT#A6	FAIL#A6
SAS #A7	J8	ACT#A7	FAIL#A7
SAS #A8	J9	ACT#A8	FAIL#A8
SAS #A9	J10	ACT#A9	FAIL#A9
SAS #A10	J11	ACT#A10	FAIL#A10
SAS #A11	J12	ACT#A11	FAIL#A11
NVMe #B0*	J13	ACT#B0	FAIL#B0**
NVMe #B1*	J14	ACT#B1	FAIL#B1**
NVMe #B2*	J15	ACT#B2	FAIL#B2**
NVMe #B3*	J16	ACT#B3	FAIL#B3**
SAS #B4	J17	ACT#B4	FAIL#B4
SAS #B5	J18	ACT#B5	FAIL#B5
SAS #B6	J19	ACT#B6	FAIL#B6
SAS #B7	J20	ACT#B7	FAIL#B7
SAS #B8	J21	ACT#B8	FAIL#B8
SAS #B9	J22	ACT#B9	FAIL#B9
SAS #B10	J23	ACT#B10	FAIL#B10
SAS #B11	J24	ACT#B11	FAIL#B11

*NVMe ports

**For NVMe ports, this failure LED is bi-color, as described on the next page.

Failure LED States	
Color and State	Indication
Red, solid	Failure
Red, blinking at 1Hz	Rebuild
Red, blinking at 4Hz	Identify
Amber, blinking	Attention! Do not remove NVMe device
Green	NVMe device ready be removed

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